



IEEE DASC 1076.1 Working Group

<http://www.eda.org/vhdl-ams/>

**Working Group Meeting
September 27, 2005
FDL'05, Lausanne, Switzerland**

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Meeting Agenda

- ◆ **WG activities (Alain Vachoux)**
- ◆ **VHDL-AMS model development (Joachim Haase)**
- ◆ **VHDL-AMS language direction (Ernst Christen)**
- ◆ **Tool experiences (Tom Kazmierski)**
- ◆ **Other business (Alain Vachoux)**
- ◆ **Next meeting (Alain Vachoux)**



WG Activities (1)

◆ WG Operating Procedures

- Adjust procedures to reflect new DASC Operating Procedures
- Draft available, based on DASC outline

◆ Put to vote by eligible WG members, deadline Sept. 23

- # eligible voting members: 13
- # returned votes: 10 (9 from DASC members + 1 from non DASC member)

Ashenden Peter	appr.	
Christen Ernst	appr.	
Haase Joachim	appr.	
Hervé Yannick	appr.	
Karffalt Jake	appr.	(not DASC member)
Mirmak Michael	appr.	
Shi Richard	appr.	
Shields John	appr.	
Smith David W.	appr.	
Vachoux Alain	appr.	

- 2/3rd approval required => vote passed



WG Activities (2)

- ◆ **WG Operating Procedures will then be reviewed for DASC approval**

- ◆ **P1076.1 Revision**
 - IR resolutions ongoing, errata sheet completed
 - Investigating the impact of the delay in VHDL 200x
 - Considering moving back on a 1076-2002 base with some exceptions
 - IR2032: Predefined function now is not pure
 - IR2042: Architecture as a block causes problems
 - LRM editorial work funding through ECSI
 - Updated schedule ready by end of October



Discussion topics

◆ VHDL-AMS model development

- Reports from activities of various groups (SAE, VDA, BEAMS, etc.)

◆ VHDL-AMS language direction

- Wrap-up of the two last meetings and first elements for continuation work

◆ Tool experiences

- Report on VHDL-AMS use in the context of analog synthesis

◆ Other business



Next WG Meetings

- ◆ DATE'06, Munich, Germany (March 6-10, 2006)
- ◆ DAC'06, San Francisco, USA (July 24-28, 2006)

